

A High Performance CMOS Current Mirror with Neuron MOSFETs and a Transimpedance Amplifier

The current being "copied" can be, and often is, a varying signal current. The current mirror is often used to provide bias currents and active loads in amplifier stages. The ideal block level concept of the ...

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The current-mirror topology is chosen due to its low input resistance and highly linear current amplification. The proposed TIA employs negative ...

This article presents a current mode transimpedance amplifier (TIA) implemented in a standard 65-nm CMOS technology for the applications of elder-care home moni

The current-mirror topology is chosen due to its low input resistance and highly linear current amplification. The proposed TIA employs negative feedback and shunt-inductive peaking to ...

An op amp transimpedance amplifier (current-to-voltage converter) can be used to convert a low input current range, to an specific output voltage range.

In this paper, a novel RGC -based circuit utilizing a current-mirror amplifier as its booster is proposed. The booster utilizes a diode-connected transistor in its input, which introduces a small ...

Current-to-voltage converters (transimpedance amplifiers) This is not an exhaustive collection of circuits, but a com-pendium of preferred ones. Where appropriate, suggested part numbers and component ...

The purpose of this project is to demonstrate the fundamentals of a transimpedance amplifier (TIA), how to change certain parameters, and to use to detect current impulses from an avalanche photodiode ...

In this paper, a new low-power CMOS optical transimpedance amplifier (TIA) for 10 Gbps applications is proposed. The main objective of this work is to achieve low-power consumption while ...

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